PATENT ABSTRACTS OF JAPAN

(11)Publication number:

05-304169

(43) Date of publication of application: 16.11.1993

(51)Int.Cl.

H01L 21/336 H01L 29/784 H01L 21/265 H01L 29/788

H01L 29/792

(21)Application number: 04-134512

(71)Applicant: NEC CORP

(22)Date of filing:

28.04.1992

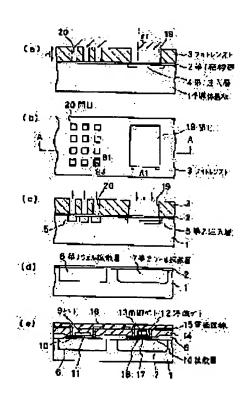
(72)Inventor: NAKADA HIDETOSHI

(54) MANUFACTURE OF SEMICONDUCTOR DEVICE

(57)Abstract:

PURPOSE: To obtain the manufacture of a semiconductor device, in which mask processes are reduced when two kinds of different diffusion layers are formed.

CONSTITUTION: The manufacture of a semiconductor device includes a process in which a mask material 3 in required thickness is formed onto a semiconductor substrate 1, a process in which a plurality of first opening 20 having opening size smaller than the thickness of the mask material are formed in a region corresponding to a first ion implanting layer to the mask material 3 while a second opening 19 is shaped extending over approximately the whole region in a region corresponding to a second ion implanting layer, a process in which ions are implanted at an angle where the semiconductor substrate 1 is not exposed to the first openings 20, and a process in which ions are implanted at an angle where the semiconductor substrate 1 is exposed to the first openings 20.



LEGAL STATUS

[Date of request for examination]

28.03.1996

[Date of sending the examiner's decision of

10.11.1998

rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration

[Date of final disposal for application]

[Patent number]